EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	106526	(design$4 or implement$4)same message	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/09 15:45
L2	6535	(memory or storage)near9 thread and execut$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/09 15:45
L3	23761	integrated near5 circuit and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/09 15:45
L4	146729	memory same interfac$4 same (connect$4 or interconnect$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/09 15:45
L5	47	L1 and L2 and L3 and L4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/09 15:54
L8	43	implement$4 near9 program$5 and l5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/09 16:14
L9	19	driv$4 same design$3 same tool and l5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/09 16:14
L10	19	implement$4 near9 program$5 and l9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/09 16:41